



PuZhi Product Guide

Focus on ARM/FPGA Solutions

PuZhi Electronic Technology (Shanghai) Co. , Ltd

www.puzhitech.com



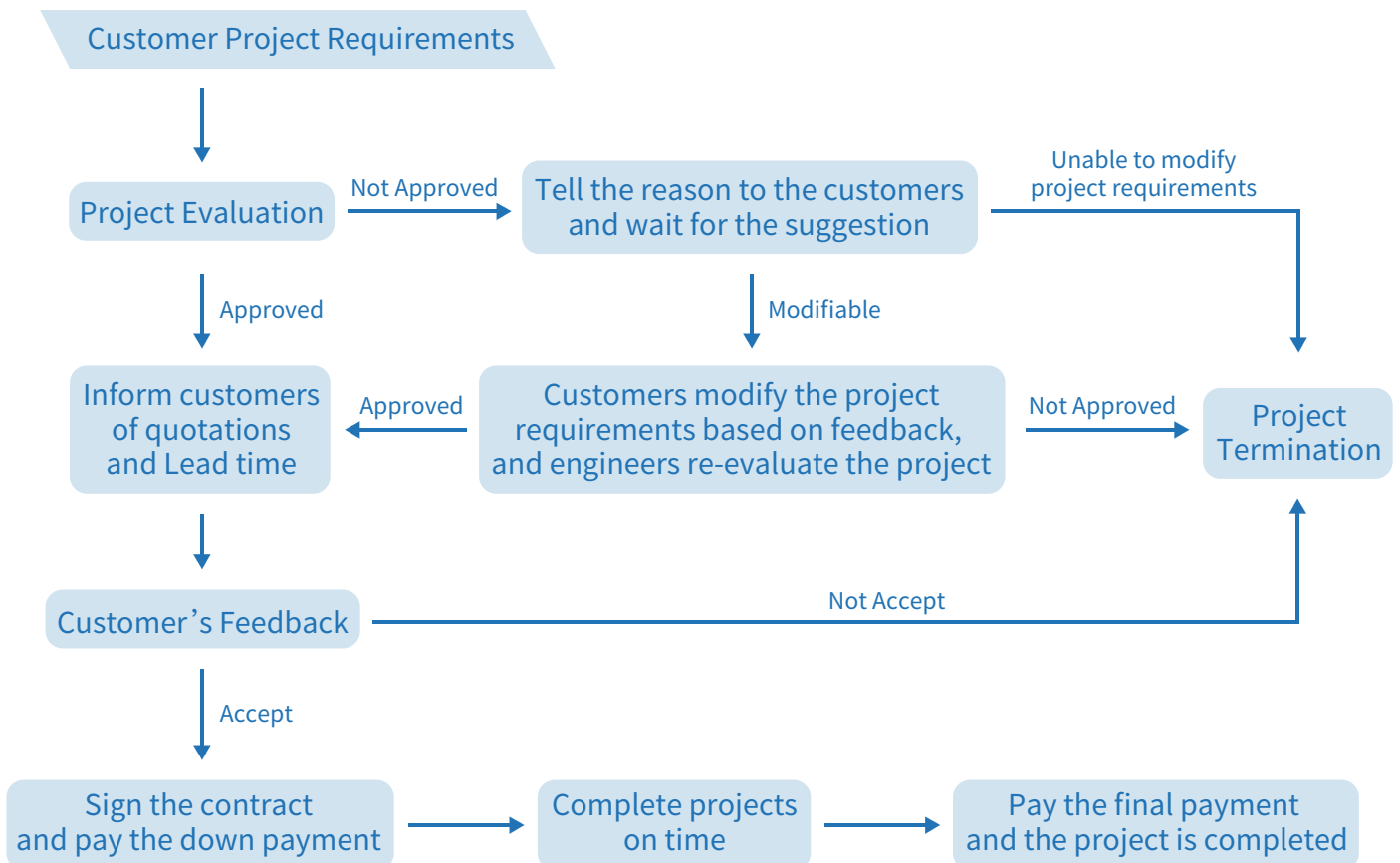
/// Product Customization Introduction ///

The product customization business is mainly for enterprises and research institutes, and the product hardware and software design is based on the customer's requirements.

/// Scope of Services ///

- Hardware Circuit Design
- Logical Design
- Linux Underlying Driver
- Embedded Software Design
- AI Algorithm Development

/// Product Customization Process ///



/// Contact information for product customization ///

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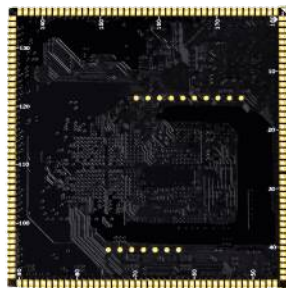
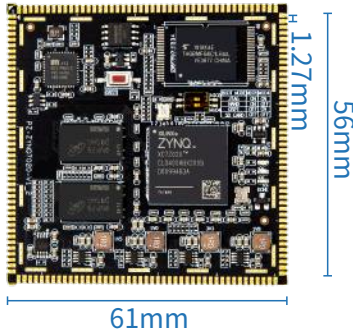
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PZ7010 | PZ7020 SOM

SPECIFICATION



Model	PZAC7010-C	PZAC7010	PZAC7020
FPGA Chip	XC7Z010-2CLG400C	XC7Z010-2CLG400I	XC7Z020-2CLG400I
ARM Main Frequency	667MHz	667MHz	766MHz
Logic Cells	28K	28K	85K
Lookup Tables (LUTs)	17600	17600	53200
Flip-Flops	35200	35200	106300
DSP Slices	80	80	220
Block RAM	2.1Mb	2.1Mb	4.9Mb
DDR3/DD3L	512MB	512MB	1GB
QSPI Flash	16MB, to store startup files and user files		
EMMC	4GB	4GB	8GB
Start-up Mode	JTAG/QSPI/NAND/SD, Onboard Dial Switch Selection		
Gigabit Ethernet	1 (industrial grade chip selected according to availability)		
User LED	2		
IO	24 x PS Ports	24 x PS Ports	24 x PS Ports
	94 x PS Ports	94 x PS Ports	118 x PS Ports
Working Temperature	0°C ~ +70°C	-40°C ~ +85°C	-40°C ~ +85°C
Technology	Immersion Gold Process, 1.27mm Stamp Hole		
Voltage/Current	5V/1A		

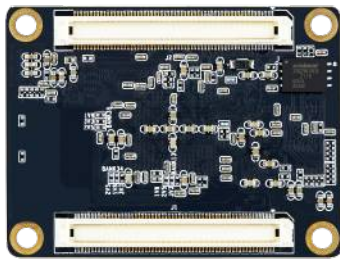
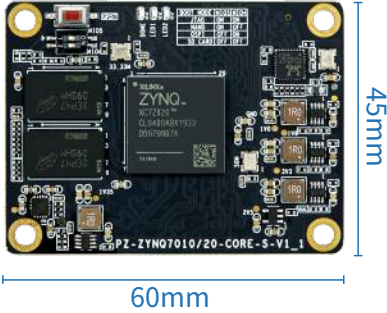
PZ7010 | PZ7020 Evaluation Kit

SPECIFICATION

FPGA Chip	XC7Z010-2CLG400C	XC7Z020-2CLG400I
Logic Cells	28K LCs	85K LCs
ARM Main Frequency	667MHz	767MHz
DDR3	512MB	1GB
EMMC	4GB	8GB
QSPI FLASH	16MB	
E2PROM	64Kbit	
JTAG Downloader	1	
UART/RS485/CAN	1/1/1	
SD Card Slot	1	
USB2.0 Host/Slave	1/1	
Gigabit Ethernet	PS Side 1/PL Side 1	
HDMI	1 HDMI Output	
LED, KEYS	5 LEDs (High Level), 4 Keys (Low Level)	
40P FPC Ports	24 Signals/12 Differential Pairs (PL Side)	
2*40P 0.1inch Ports	56 single-ended 27 Pairs Differential (PL Side)	
Form Factors	128mm * 96mm	
Technology	Black Matte, Immersion Gold Process	



PZ7010S | PZ7020S SOM



SPECIFICATION

Model	PZAC7Z010-C	PZAC7Z010	PZAC7Z020
FPGA Chip	XC7Z010-2CLG400C	XC7Z010-2CLG400I	XC7Z020-2CLG400I
ARM Main Frequency	667MHz	667MHz	766MHz
Logic Cells	28K	28K	85K
Flip-Flops	17600	17600	53200
Lookup Tables (LUTs)	35200	35200	106300
DSP slice	80	80	220
BLOCK RAM	2.1Mb	2.1Mb	4.9Mb
DDR3/DD3L	512MB	512MB	1GB
QSPIFLASH	32MB	32MB	32MB
Gigabit Ethernet	1 (industrial grade chip selected according to availability)		
User LED	2		
	30 PS ports (24 fixed 1.8V, 6 fixed 3.3V)		
IO	92 PL ports (1.8/2.5/3.3V adjustable)	92 PL ports (1.8/2.5/3.3V adjustable)	116 PL ports (1.8/2.5/3.3V adjustable)
Working Temperature	0°C~+70°C	-40°C~+85°C	-40°C~+85°C
Technology	Immersion Gold Process, 0.6mm Pitch 120P Connector		
Voltage/Current	5V/1A		

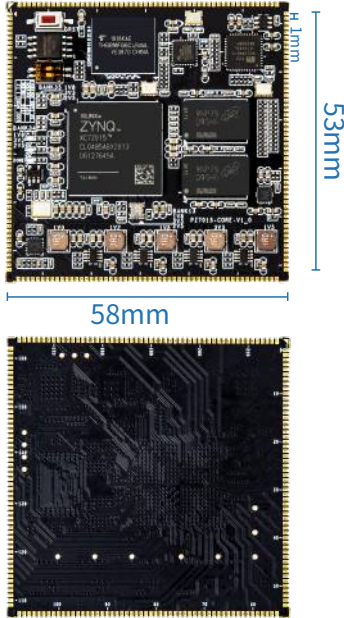
PZ7010S | PZ7020S Evaluation Kit

SPECIFICATION

FPGA Chip	XC7Z010-2CLG400C	XC7Z020-2CLG400I
Logic Cells	28K LCs	85K LCs
ARM Main Frequency	667MHz	767MHz
DDR3	512MB	1GB
QSPI FLASH	32MB	
E2PROM	64Kbit	
JTAG Downloader	1	
UART/RS485/CAN	1/1/1	
SD Card Slot	1	
USB2.0 Host/Slave	1/1	
Gigabit Ethernet	PS Side 1/PL Side 1	
HDMI	1 HDMI Output	
LED, KEYS	4 LEDs, 4 Keys	
2*40P 0.1inch Ports	32 Single-ended/16 Pairs Differential (PL side)	
Expansion Port	68 single-ended/34 differential pairs	
Form Factors	120mm * 90mm	
Technology	Black Matte, Immersion Gold Process	



PZ7015 SOM



SPECIFICATION

FPGA Chip	XC7Z015-2CLG485I
Processor Core	Dual-Core ARM Cortex-A9, Main Frequency 766MHz
Logic Cells	74K
DSP Slices	160 (18x25MACCs)
DDR3/DD3L	1GB
QSPI FLASH	16MB, to store startup files and user files
EMMC	8GB, to store startup files and user files
Start-up Mode	JTAG/QSPI/SD, Onboard Dial Switch Selection
Gigabit Ethernet	1 (industrial grade chip selected according to availability)
USB2.0	1 channel, compatible with master-slave interface
User LED	2
IO	12 PS ports, 140 PL ports (IO level 1.8/2.5/3.3 adjustable)
GTX	1 BANK, 4 Paris of TX/RX each
Voltage/Current	5V/1A
Working Temperature	-40°C ~ +85°C
Technology	Immersion Gold Process, 1.0mm Stamp Hole

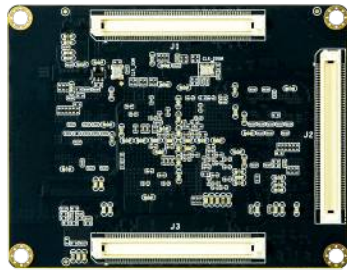
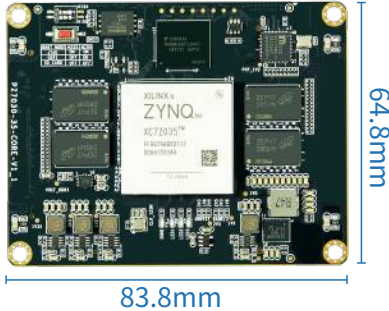
SPECIFICATION

FPGA Chip	XC7Z015-2CLG485I
Logic Cells	74K
Look-Up Tables (LUTs)	46200
Flip-Flops	92400
Total Block RAM	3.3Mb
DDR3	1GB
EMMC	8GB
QSPI FLASH	16MB
E2PROM	64Kbit
JTAG Downloader	1
UART/RS485/CAN	1/1/1
SD Card	1
USB2.0	1/1
Gigabit Ethernet	1 Channel on PS and PL side
HDMI	1 Channel HDMI output
LED, Key	5 LEDs (Hight Level), 4 Keys (Low Level)
SFP	2
PCIE2.0	x 2
40Px2 Ports	64 single-ended/32 pairs of differential
Power Supply	12V/2A
SOM Form Factors	58x58mm
Board Form Factors	167x104mm

PZ7015 Evaluation Kit



PZ7030 | PZ7035 SOM



SPECIFICATION

FPGA Chip	XC7Z030-2FFG676I	XC7Z035-2FFG676I
Processor Core	Dual-Core ARM Cortex-A9, Main Frequency 800Mhz	
Logic Cells	125K	275K
Lookup Tables (LUTs)	78600	171900
Flip-Flops	157200	343800
DSP Slices	400	900
BLOCK RAM	9.3Mb	17.6Mb
DDR3/DD3L	PS-side 1GB, PL-side 1GB	
QSPI FLASH	32Mb, to store startup files and user files	
EMMC	8GB, to store startup files and user files	
Start-up Mode	JTAG/QSPI/SD, Onboard Dial Switch Selection	
Gigabit Ethernet	1 (industrial grade chip selected if availability)	
User LED	3	
IO	24 PS Ports, 164 PL Ports (48 IO level fixed 1.8V; 192 IO level 1.8/2.5/3.3 adjustable)	
GTX	TX/TX x 4	TX/RX x 8
Voltage/Current	5V / 3A	
Working Temperature	-40°C~+85°C	
Technology	Immersion Gold Process, 120P Connectors x 3	

PZ7030 | PZ7035 Evaluation Kit

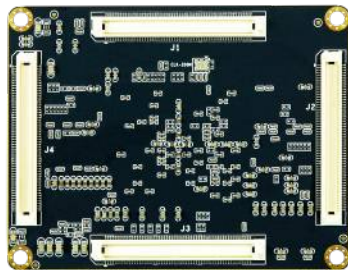
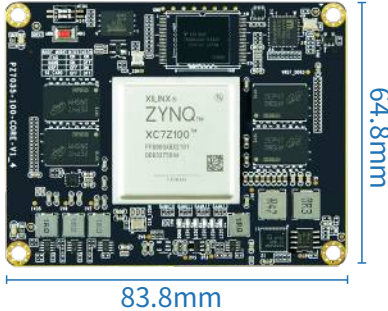
SPECIFICATION

FPGA Chip	XC7Z030-2FFG676I	XC7Z035-2FFG676I
Logic Cells	125K	275K
Lookup Tables (LUTs)	78600	171900
Flip-Flops	157200	343800
BLOCK ROM	9.3Mb	17.6Mb
DSP Slices	400	900
DDR3/DD3L	PS-side 1GB / PL-side 1GB	
QSPI FLASH	32Mb, to store startup files and user files	
EMMC	8GB, to store startup files and user files	
Start-up Mode	JTAG/QSPI/SD, Onboard Dial Switch Selection	
Serial/RS485/CAN	1/1/1	
SD Card Slot	1	
Downloader (onboard)	1	
USB2.0 TypeA	4	
HDMI Output	1	
Gigabit Ethernet	2 channels, PS side 1, PL side 1	
User LED/ User Key	5/5	
SFP Interface	2	
PCIE2.0	x4 Interface (Only for PZ7035)	
IO	112 PL ports (32 IO fixed 1.8V, 80 IO level 1.8/2.5/3.3 adjustable)	
Voltage/Current	12V/2A	
Technology	Immersion gold process, 0.6mm pitch 120P connector x 3	



PZ7035 | PZ7045 | PZ7100 SOM

SPECIFICATION



FPGA Chip	XC7Z035-2FFG900I	XC7Z045-2FFG900I	XC7Z100-2FFG900I
Processor Core	Dual-Core ARM Cortex-A9, Main Frequency 800Mhz		
Logic Cells	275K	350K	444K
Lookup Tables (LUTs)	171900	218600	277400
Flip-Flops	343800	437200	554800
DSP Slices	900	900	2020
BLOCK RAM	17.6Mb	19.2Mb	26.5Mb
DDR3/DD3L	PS-side 1GB, PL-side 1GB		
QSPI FLASH	32Mb, to store startup files and user files		
EMMC	8GB, to store startup files and user files		
Start-up Mode	JTAG/QSPI/SD, Onboard Dial Switch Selection		
Gigabit Ethernet	1 (industrial grade chip selected if availability)		
User LED	5		
IO	24 PS Ports, 240 PL Ports (48 IO level fixed 1.8V; 192 IO level 1.8/2.5/3.3 adjustable)		
GTX	4 BANK, 16 Paris of TX/RX each		
Voltage/Current	12V/3A		
Working Temperature	-40°C~+85°C		
Form Factors	83.8x63.8mm, Immersion Gold Process		
Technology	0.6mm 120P Connectors x 4		

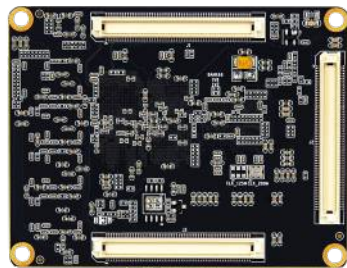
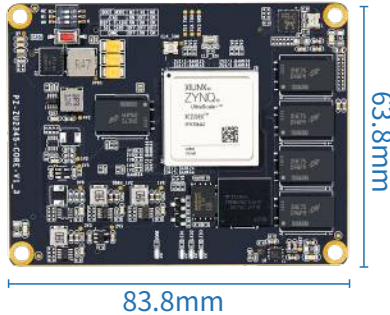
PZ7035 | PZ7045 | PZ7100 Evaluation Kit

SPECIFICATION

FPGA Chip	XC7Z035-2FFG900I	XC7Z045-2FFG900I	XC7Z100-2FFG900I
Processor Core	Dual Core ARM Cortex-A9, Main Frequency 800Mhz		
Logic Cells	275K	350K	444K
Lookup Tables (LUTs)	171900	218600	277400
Flip-Flops	343800	437200	554800
DSP Slices	900	900	2020
BLOCK RAM	17.6Mb	19.2Mb	26.5Mb
DDR3/DD3L	Total 2GB, PS side 1GB/ PL side 1GB		
QSPI FLASH	32Mb, to store startup files and user files		
EMMC	8GB, to store startup files and user files		
Start-up Mode	JTAG/QSPI/SD, Onboard Dial Switch Selection		
JTAG	Upgraded to USB to JTAG		
UART/RS485/CAN	1/1/1		
SD Card Slot	1		
USB	4 USB Host, Connect to peripherals		
Gigabit Ethernet	2 channels, PS side 1, PL side 1		
HDMI	1 input/1 output compatible		
SATA	1		
SFP	4		
PCIE2.0	x 8 Interfaces		
User LED	5		
Key	5		
Fan Connector	1 channel, 12V power supply		
IO	168 single-ended/84 differential pairs JM1 interface level 1.8V,		
Expansion Port	JM2/JM3/J23 interface level 1.8V/2.5V/3.3V adjustable		
GTX	4 BANK, 16 Paris of TX/RX each		
Voltage/Current	12V/2A		
Working Temperature	-40°C~+85°C (Industrial grade)		
Core Board Size	83.8mm x 63.8mm		
Carrier Board Size	220mm x 116mm		



PZ-ZU2CG | PZ-ZU3EG SOM



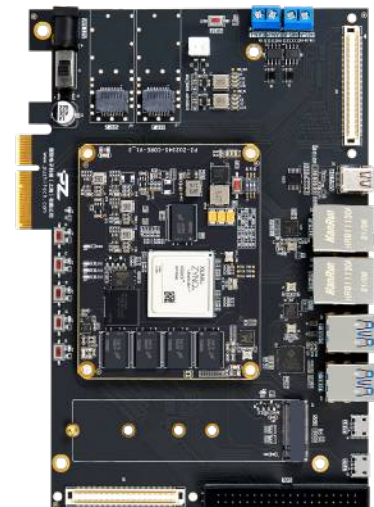
SPECIFICATION

FPGA Chip	XCZU2CG-2SFVC784I	XCZU5EV-2SFVC784I
Processor Core	2 x Cortex-A53-1.2Ghz 2 x Cortex-R5-500Mhz	4 x Cortex-A53-1.2Ghz 2 x Cortex-R5-500Mhz
Logic Cells (K)	103	154
CLB Flip-Flops (K)	94	141
CLB LUTs (K)	47	71
BLOCK ROM (Mb)	5.3	7.6
DSP Slices	240	360
DDR4	PS Side 4GB, PL Side 1GB	
QSPI FLASH	32MB, to store startup files and user files	
EMMC	8GB	
Start-up Mode	JTAG/QSPI/SD/EMMC, Boot from SD Card in Default	
Gigabit Ethernet	1	
User LED	3	
Reset Key	1	
PS-side GTR	4 paris of TX/RX	
IOs	38 IOs on PS, 192 IOs on PL (1.2/1.8/2.5/3.3 adjustable)	
Voltage/Current	5V/3A	
Technology	Immersion Gold Process, 120P Connectors x 3	
Connector Height	3mm	

PZ-ZU2CG | PZ-ZU3EG Evaluation Kit

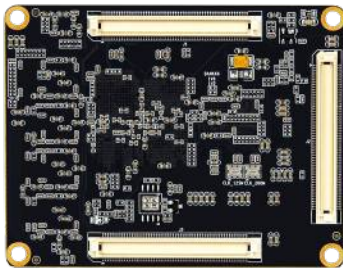
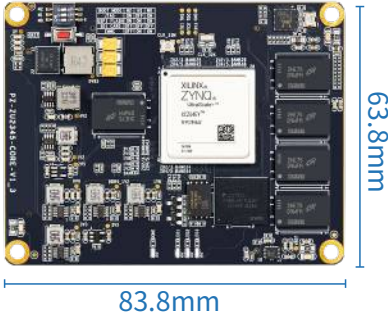
SPECIFICATION

FPGA Chip	XCZU2CG-2SFVC784I	XCZU5EV-2SFVC784I
Processor Core	2 x Cortex-A53-1.2Ghz 2 x Cortex-R5-500Mhz	4 x Cortex-A53-1.2Ghz 2 x Cortex-R5-500Mhz
Logic Cells (K)	103	154
CLB Flip-Flops (K)	94	141
CLB LUTs (K)	47	71
BLOCK ROM (Mb)	5.3	7.6
DSP Slices	240	360
DDR4	PS side 4GB, PL side 1GB	
QSPI FLASH	32MB	
EMMC	8GB	
E2PROM	64Kbit	
UART/RS485/CAN	1/1/1	
SD Card	1	
Gigabit Ethernet	PS end 1 Port, PL end 1 Port	
Mini DP Output	1	
RTC	1 on Core Board, 1 reserved on FPGA Board	
LED, KEY	5 LEDs (High-Level), 5 KEYS (Low-Level)	
SSD Storage	1 (PS end)	
JTAG Downloader	1 (Integrated on Board)	
40P Expansipn Port	1 Port, 32 single-ended/16 pairs of differential	
120P Expansipn Port	2 Ports, 80 single-ended/40 pairs of differential	



PZ-ZU4EV | PZ-ZU5EV SOM

SPECIFICATION

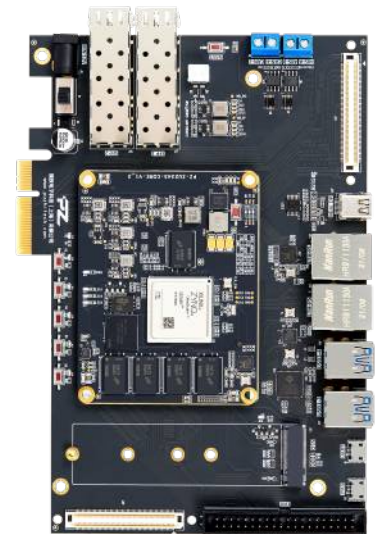


FPGA Chip	XCZU4EV-2SFVC784I	XCZU5EV-2SFVC784I
Processor Core	4 x Cortex-A53-1.3Ghz、 2 x Cortex-R5-533Mhz	
Logic Cells (K)	192	256
CLB Flip-Flops (K)	176	234
CLB LUTs (K)	88	117
BLOCK ROM (Mb)	4.5	5.1
UltraRAM (Mb)	13.5	18
DSP Slices	728	1248
Video Codec	H264/H265	
DDR4	PS Side 4GB, PL Side 1GB	
QSPI FLASH	32MB, to store startup files and user files	
EMMC	8GB	
Start-up Mode	JTAG/QSPI/SD/EMMC, Boot from SD Card in Default	
Gigabit Ethernet	1	
User LED	3	
Reset Key	1	
PS-side GTR	4 paris of TX/RX	
PL-side GTH	4 paris of TX/RX	
IOs	38 IOs on PS, 192 IOs on PL (1.2/1.8/2.5/3.3 adjustable)	
Voltage/Current	5V/3A	
Technology	Immersion Gold Process, 120P Connectors x 3	
Connector Height	3mm	

PZ-ZU4EV | PZ-ZU5EV Evaluation Kit

SPECIFICATION

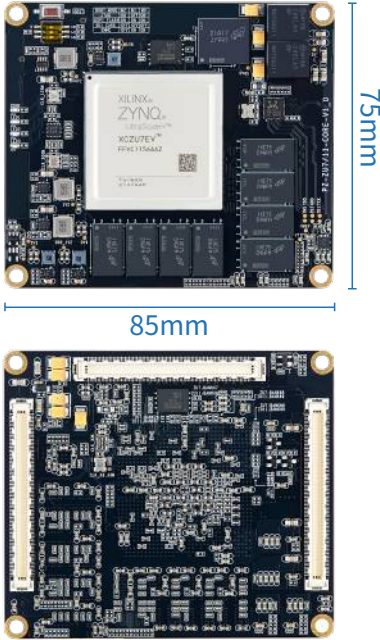
FPGA Chip	XCZU4EV-2SFVC784I	XCZU5EV-2SFVC784I
Processor Core	4 x Cortex-A53-1.3Ghz、 2 x Cortex-R5-533Mhz	
Logic Cells (K)	192	256
CLB Flip-Flops (K)	176	234
CLB LUTs (K)	88	117
BLOCK ROM (Mb)	4.5	5.1
UltraRAM (Mb)	13.5	18
DSP Slices	728	1248
DDR4	PS side 4GB, PL side 1GB	
QSPI FLASH	32MB	
EMMC	8GB	
E2PROM	64Kbit	
UART/RS485/CAN	1/1/1	
SD Card	1	
Gigabit Ethernet	PS end 1 Port, PL end 1 Port	
Mini DP Output	1	
RTC	1 on Core Board, 1 reserved on FPGA Board	
LED, KEY	5 LEDs (High-Level), 5 KEYS (Low-Level)	
PCIe3.0	x 2	
SFP	2	
SSD Storage	1 (PS end)	
JTAG Downloader	1 (Integrated on Board)	
40P Expansipn Port	1 Port, 32 single-ended /16 pairs of differential	
120P Expansipn Port	2 Ports, 80 single-ended /40 pairs of differential	





PZ-ZU7EG | PZ-ZU7EV | PZ-ZU11EG SOM

SPECIFICATION



FPGA Chip	XCZU7EG-2FFVC1156I XCZU7EV-2FFVC1156I XCZU11EG-2FFVC1156I		
Processor Core	ARM : 4 x Cortex-A53 1.3Ghz RPU : 2 x Cortex-R5 533Mhz GPU : Mali-400MP2 667Mhz		
Logic Cells (K)	504K	504K	653K
CLB LUTs (K)	230K	230K	299K
CLB Flip-Flops (K)	461K	461K	597K
Block RAM	11Mb	11Mb	21.1Mb
UltraRAM	27Mb	27Mb	22.5Mb
DSP Slices	1728	1728	2928
Video Codec	- H.264 / H.265 -		
DDR4/DDR4L	PS 4GB 2400Mhz*64bit / PL 4GB 2400Mhz*64bit		
QSPI FLASH	2*32MB (QSP0+QSPI1), 64MB in Total		
EMMC	8GB, stores boot files and user files		
Start-up Mode	JTAG/QSPI/SD/EMMC, On-board DIP Selection		
Gig a bit Ethernet	1		
User LED	3 LEDs (High-Level)		
IOs	MIO: 38 (1.8V Fixed Voltage) HP: 144 (1.2/1.8V adjustable, 1.8V by default) HD: 48 (1.8/2.5/3.3Vadjustable, 1.8V by default)		
GTR/GTH	PS 4 Pairs of TX/RX, PL 20 Pairs of TX/RX		
Voltage/Current	8-12V/5A (Recommend on 8V)		
Working Temperature	-40°C -- +85°C		
Technology	Immersion Gold Process, 168P Connectors x 3		
Connector Height	4 mm		

PZ-ZU7EG | PZ-ZU7EV | PZ-ZU11EG Evaluation Kit

SPECIFICATION

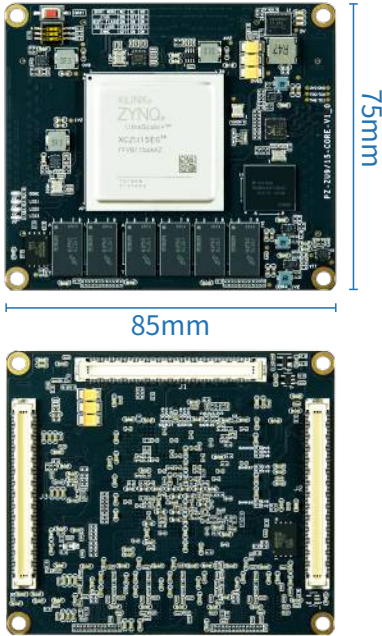
FPGA Chip	XCZU7EG-2FFVC1156I XCZU7EV-2FFVC1156I XCZU11EG-2FFVC1156I		
Processor Core	ARM : 4 x Cortex-A53 1.3Ghz RPU : 2 x Cortex-R5 533Mhz GPU : Mali-400MP2 667Mhz		
logic cells	504K	504K	653K
Lookup Tables (K)	230K	230K	299K
Flip-Flops	461K	461K	597K
Block RAM	11Mb	11Mb	21.1Mb
Ultra RAM	27Mb	27Mb	22.5Mb
DSP Slices	1728	1728	2928
Video Codec	- H.264 / H.265 -		
DDR4	PS 4GB/ PL 4GB		
EMMC	8GB		
QSPI FLASH	64MB, Dual FLASH Boot, Faster Boot		
Start-up Mode	JTAG/SD/QSPI/EMMC		
E2PROM	64Kbit		
UART/RS485/CAN	1 / 1 / 1		
SD Card Slot	1		
Gigabit Ethernet	PS-side 1/PL-side 1		
USB3.0	4 Ports		
USB to JTAG	1		
Mini DP Interface	1		
HDMI (4K)	1 Input / 1 Output		
SSD (NVME Protocol)	1		
SFP	2		
SATA	2		
MIPI	2		
FMC	HPC		
Expansion Port	40P2.54 Interface /120P Ports		
PCIe3.0	x 4		
LED	3 LEDs in SOM/2 LEDs in Carrier Board		
Keys	2		
Voltage/Current	12V/3A		





PZ-ZU9EG | PZ-ZU15EG SOM

SPECIFICATION



FPGA Chip	XCZU9EG-2FFVB1156I	XCZU15EG-2FFVB1156I
Processor Core	Quad-Core Arm® Cortex®-A53 (1.333Ghz), Dual Core Arm Cortex-R5F (533Mhz)	
GPU	Mali™-400 MP2, 667Mhz	
Logic Cells	600	747
Lookup Tables (LUTs)	274	341
Flip-Flops (K)	548	682
Max.DistributedRAM (Mb)	8.8	11.3
Block RAM (Mb)	32.1	26.2
Ultra RAM (Mb)	---	31.5
DSP Slices	2520	3528
DDR4	PS-side 4GB,PL-side 2GB	
EMMC	8GB	
QSPI FLASH	64MB, Dual FLASH Boot, Faster Boot	
Start-up Mode	JTAG/SD/QSPI/EMMC	
Gigabit Ethernet	1	
User LED	3	
IO	38 IOs on PS side (fixed 1.8V) 192 IOs on the PL side (96 1.2/1.8V adjustable 96 1.8/2.5/3.3V adjustable)	
GTH	6 BANK, a total of 24 pairs of TX/RX Speed support 16.25Gb/s	
Voltage/Current	8~12V/4A	
Working Temperature	-40~85°C	
Technology	Immersion Gold Process, 168P Connectors x 3	

PZ-ZU9EG | PZ-ZU15EG Evaluation Kit

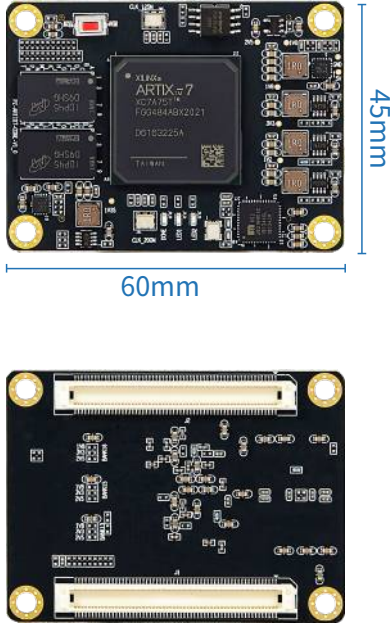
SPECIFICATION

FPGA Chip	XCZU9EG-2FFVB1156I	XCZU15EG-2FFVB1156I
Processor Core	Quad-Core Arm® Cortex®-A53 (1.333Ghz), Dual Core Arm Cortex-R5F (533Mhz)	
GPU	Mali™-400 MP2, 667Mhz	
Logic Cells (K)	600	747
Lookup Tables (K)	274	341
Flip-Flops (K)	548	682
Max.DistributedRAM (Mb)	8.8	11.3
Block RAM (Mb)	32.1	26.2
Ultra RAM (Mb)	---	31.5
DSP Slices	2520	3528
DDR4	PS-side 4GB,PL-side 2GB	
EMMC	8GB	
QSPI FLASH	64MB, Dual FLASH Boot, Faster Boot	
Start-up Mode	JTAG/SD/QSPI/EMMC	
E2PROM	64Kbit	
UART/RS485/CAN	1/1/1	
SD Card Slot	1	
Gigabit Ethernet	PS-side 1/PL-side 1	
USB3.0	4 Ports	
USB to JTAG	1	
Mini DP Interface	1	
HDMI (4K)	1 Input/1 Output	
SSD (NVME Protocol)	1	
SFP	2	
SATA	2	
MIPI	2	
FMC	HPC FMC Interface	
Expansion Port	2	
LED	3 LEDs in SOM/4 LEDs in Carrier Board	
Keys	3	
Voltage/Current	12V/3A	





PZ-A735T | PZ-A775T | PZ-A7100T | PZ-A7200T SOM



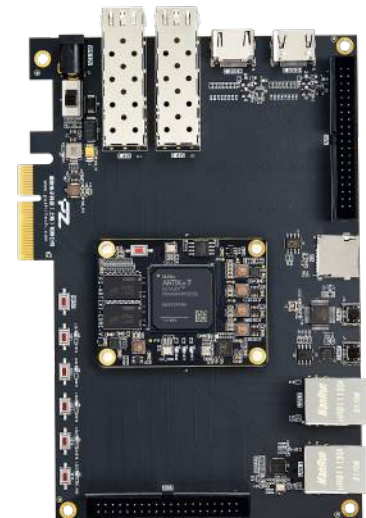
SPECIFICATION

FPGA Chip	XC7A35T-2FGG484I	XC7A75T-2FGG484I	XC7A100T-2FGG484I	XC7A200T-2FBG484I
Logic Cells	33280	75520	101440	215360
Slices	5200	11800	15850	33650
BLOCK ROM	1800	3780	4846	13140
DSP Slices	90	180	240	740
DDR3/DD3L	1GB/32bit bit width			
QSPI FLASH	32MB, to store startup files and user files			
Start-up Mode	JTAG/QSPI, default QSPI enabled			
Gigabit Ethernet	1 (industrial grade chip selected if availability)			
User LED	2			
Reset Key	1			
GTP	4 pairs of TX/RX			
IO	138 IOs	172 IOs	172 IOs	172 IOs
Voltage/Current	5V/2A			
Technology	Immersion Gold Process, 120P Connectors x 2			
Connector Height	3mm			

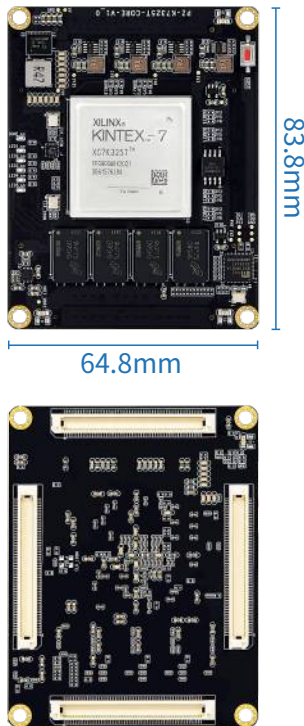
PZ-A735T | PZ-A775T | PZ-A7100T | PZ-A7200T Evaluation Kit

SPECIFICATION

FPGA Chips	XC7A35T-2FGG484I	XC7A75T-2FGG484I	XC7A100T-2FGG484I	XC7A200T-2FBG484I
Logic Cells	33280	75520	101440	215360
Slices	5200	11800	15850	33650
BLOCK ROM (Kb)	1800	3780	4846	13140
DSP Slices	90	180	240	740
DDR3	1GB			
QSPI FLASH	32MB			
E2PROM	64Kbit			
JTAG Downloader	1			
UART	1			
SD Card	1			
Gigabite Ethernet	2			
HDMI	1 HDMI Input Output/1 HDMI Output			
LED, KEYS	5 LEDs (High Level), 5 KEYS (Low Level)			
SFP	2			
PCIE2.0	1 * PCIE 2.0 X 2			
40P x 2 Ports	62 single-ended/32 differential pairs			
Power Input	12V / 2A			
SOM Form Factors	60 x 45mm			
Board Form Factors	172 x 116mm			



PZ-K7325T | PZ-K7410T SOM



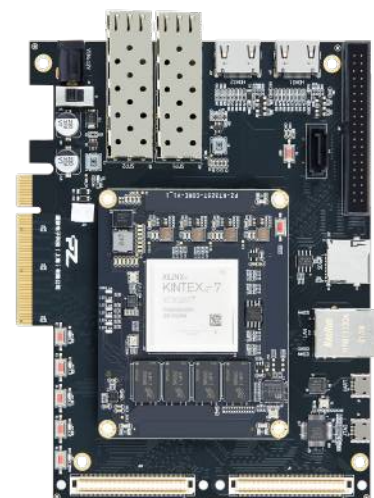
SPECIFICATION

FPGA Chip	XC7K325T-2FFG900I	XC7K410T-2FFG900I
Logic Cells	326080	406720
Slices	50950	63550
BLOCK ROM	16020	28620
DSP Slices	840	1540
DDR3/DD3L	2GB/32bit bit width	
QSPI FLASH	32Mb, to store startup files and user files	
Start-up Mode	JTAG/QSPI, default QSPI enabled	
Gigabit Ethernet	1 (industrial grade chip selected if availability)	
User LED	5	
Reset Key	1	
GTX	16 pairs of TX/RX	
IO	288 IO ports, all level 1.8/2.5/3.3V adjustable	
Voltage/Current	5V/3A	
Technology	Immersion Gold Process, 120P Connectors x 4	
Connector Height	3mm	

PZ-K7325T | PZ-K7410T Evaluation Kit

SPECIFICATION

FPGA Chip	XC7K325T-2FFG900I	XC7K410T-2FFG900I
Slices	50,950	63,550
Logic Cells	326,080	406,720
CLB Flip-Flops	407,600	508,400
Total Block RAM (kB)	16,020	28,620
DDR3	2GB	
QSPI FLASH	32MB	
E2PROM	64Kbit	
UART	1	
SD Card Slot	1	
Gigabit Ethernet	1	
HDMI	1 HDMI input and output, 1 HDMI output	
LED, KEY	5 LEDs (high-level), 5 Keys (low-level)	
SATA	1	
PCIE2.0	x 8	
SFP	2	
40P Expansion Port	1, 32 Single-ended/16 Pairs of Differential	
120P Expansion Port	2, 96 single-ended/48 pairs of differential	



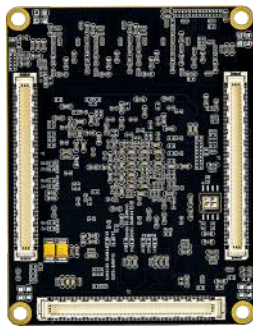


KU040 | KU060 | KU095 SOM

SPECIFICATION



64.8mm



FPGA Chip	XCKU040-2FFVA1156I	XCKU060-2FFVA1156I	XCKU095-2FFVA1156I
Logic Cells (K)	530250	725550	1176000
LUT	242400	331680	537600
Flip-Flops	484800	663630	1075200
BLOCK ROM	21.1Mb	38Mb	59.1Mb
DSP Slices	1920	2760	768
DDR4	4GB/64bit bit width		
QSPI FLASH	2 in total, 1 for SMD (32MB) /1 reserved		
Start-up Mode	JTAG/QSPI, default QSPI enabled		
Gigabit Ethernet	1		
User LED	5		
Reset Key	1		
GTH	20 Pairs of TX/RX		
IO	A total of 296 IOs		
	HR: 8 single-ended (fixed 3.3V)		
	HR: 24 pairs differential/48 single-ended (1.8/2.5/3.3V)		
HP: 120 pairs differential/240 single-ended (1.2/1.8V)			
Voltage/Current	5V/6A		
Technology	Immersion Gold Process, 168P Connectors x 3		
Connector Height	4mm		

KU040 | KU060 | KU095 Evaluation Kit

SPECIFICATION

FPGA Chip	XCKU040-2FFVA1156I	XCKU060-2FFVA1156I	XCKU095-2FFVA1156I
Logic Cells (K)	530250	725550	1176000
LUT	242400	331680	537600
Flip-Flops	484800	663630	1075200
BLOCK ROM	21.1Mb	38Mb	59.1Mb
DSP Slices	1920	2760	768
QSPI FLASH	32MB, stores boot files and user files		
E2PROM	64Kb		
Start-up Method	Default QSPI startup		
Serial Port	1		
SD Card Slot	1		
Downloader (onboard)	1		
HDMI Input (4K)	1		
HDMI Output (4K)	1		
Gigabit Ethernet	2		
User LED/ User Key	2/2		
SFP Interface	2		
PCIe3.0	x4 Interface		
40P Expansion Port	32 Single-ended/16 Pairs of Differential (3.3V)		
120P Expansion Port	48 single-ended/24 differential Pairs (1.2/1.8V Adjustable)		
FMC-HPC Port	Signal Definition Contact Customer Service		
Voltage/Current	12V / 3A		
Technology	83.8 x 64.8mm, Immersion Gold Process		

